

<b>Notice of References Cited</b>	Application/Control No. 10/734,457		Applicant(s)/Patent Under Reexamination JOHNSON ET AL.	
	Examiner Qing Chen		Art Unit 2191	Page 1 of 1

#### U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,680,645	10-1997	Russell et al.	710/48
*	B	US-5,809,450	09-1998	Chrysos et al.	702/186
*	C	US-5,812,811	09-1998	Dubey et al.	712/216
*	D	US-6,212,544 B1	04-2001	Borkenhagen et al.	718/103
*	E	US-6,480,818 B1	11-2002	Alverson et al.	703/26
*	F	US-6,535,905 B1	03-2003	Kalafatis et al.	718/108
*	G	US-6,658,447 B2	12-2003	Cota-Robles, Erik	718/103
*	H	US-6,931,641 B1	08-2005	Davis et al.	718/108
*	I	US-6,948,172 B1	09-2005	D'Souza, David	718/106
*	J	US-7,134,002 B2	11-2006	Shoemaker, Ken	712/228
*	K	US-7,134,124 B2	11-2006	Ohsawa et al.	718/100
	L	US-			
	M	US-			

#### FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

#### NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Boothe et al., "Improved Multithreading Techniques for Hiding Communication Latency in Multiprocessors," 1992, ACM, pg. 214-223.
	V	Mowry, Todd C., "Tolerating Latency in Multiprocessors through Compiler-Inserted Prefetching," 1998, ACM, pg. 55-92.
	W	Xu et al., "Dynamic Instrumentation of Threaded Applications," 1999, ACM, pg. 49-59.
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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